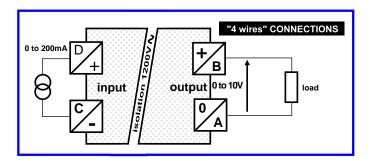
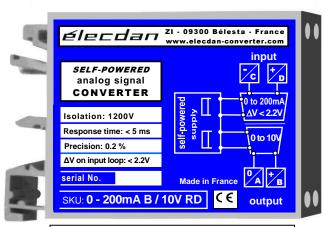


CSA 16

Isolation assured - Power supply eliminated - Reduced cabling - Increased reliability - Reduced volume & costs

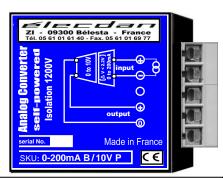




Case for DIN rail: 66 x 53 x 12.5 mm SKU: 0-200mA B / 10 V R D



Case for PRINTED CIRCUIT: 50.8 x 50.8 x 11 mm SKU: 0-200mA B / 1 0 V C I



Case for WALL mounting: 50.8 x 50.8 x 11 mm SKU: 0-200mA B / 1 0 V P This high-efficiency "passive" quadripole converts, isolates and outputs within 0 to 10V analog signals from 0 to 200mA applied to its input. It does not require any external power supply.

Input

- > 0 to 200 mA (accidental maximum: 400 mA / second) *
- > sensitivity: better than 0.5% of full scale

Output

- > output signal: 0 to 10V (load ≥ 5 kΩ) *
- regulation upon no load / on load: better than 2.10⁻³
- * Upon request: other input current and/or output voltage

Full-scale accuracy better than 0.2%

Response time < 5 ms; can be increased upon request

Isolation 1200V

Voltage on the input loop less than 2.2V for input current 200 mA

Power supply only by the low energy taken on the input loop

Protections

- against reversal of connections on the input
- against accidental overvoltage on input: up to 300 mA
- against overloads and short-circuits
- isolation: 1200 V
- increased reliability because of no external power supply
- against vibrations; tropicalisation with moulding
- > elimination of the faulty-contact risk (no DIL switch)
- > negligible temperature rise (high performance conversion)
- electromagnetic compatibility CEM 89 / 336 / CE

Temperatures

- > coefficient: < 2.10⁻⁴ / °C
- > operation: -20°C to +60°C
- > storage: -40°C to +90°C

Presentations 3 types of cases

SKU	Pre-tax price
0-200 mA B / 0-10V RD	
0-200 mA B / 0-10V CI	
0-200 mA B / 0-10V P	

Please also see our other self-powered products

- > current / voltage version:
 - 0 to 20mA (or 4 to 20mA) → 0 to 10V "ZERO INTERFERENCE"
 - 0 to 20mA → 0 to 10V
 - 0 to 2A → 0 to 10V
 - 0 to 5A → 0 to 10V
- current / current version:
 - ◆ 0 to 100mA → 0 to 100mA